"Background of the Invention" and insert the following paragraphs:

A1

-- CROSS-REFERENCE TO RELATED APPLICATIONS.

This application relates to subject matter that is similar to that of the copending application having serial number 09/459,739 which was also filed on December 10, 1999 and is assigned to the same entity.

STATEMENT REGARDING FEDERALLY-SPONSORED RESEARCH OR DEVELOPMENT.

Not Applicable.

INCORPORATION BY REFERENCE OF MATERIAL SUBMITTED ON A COMPACT DISC.

Not Applicable.

BACKGROUND OF THE INVENTION. --

At page 6, line 20, please insert the following paragraph:

A2

-- SUMMARY OF THE INVENTION.

In a system including a high speed buffer logically placed between memory and at least one processor unit, a method for executing an instruction stream stored in the memory, wherein the instruction stream comprises a sequence of instructions including at least one prefetch instruction that prefetches information from the memory into the high speed buffer, the method comprises the following steps: deriving first path data from a compiler performing static compilation, wherein the first path data represents a first path from the prefetch instruction to an instruction that uses information prefetched by the prefetch instruction; generating second path data, wherein the second path data represents a

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